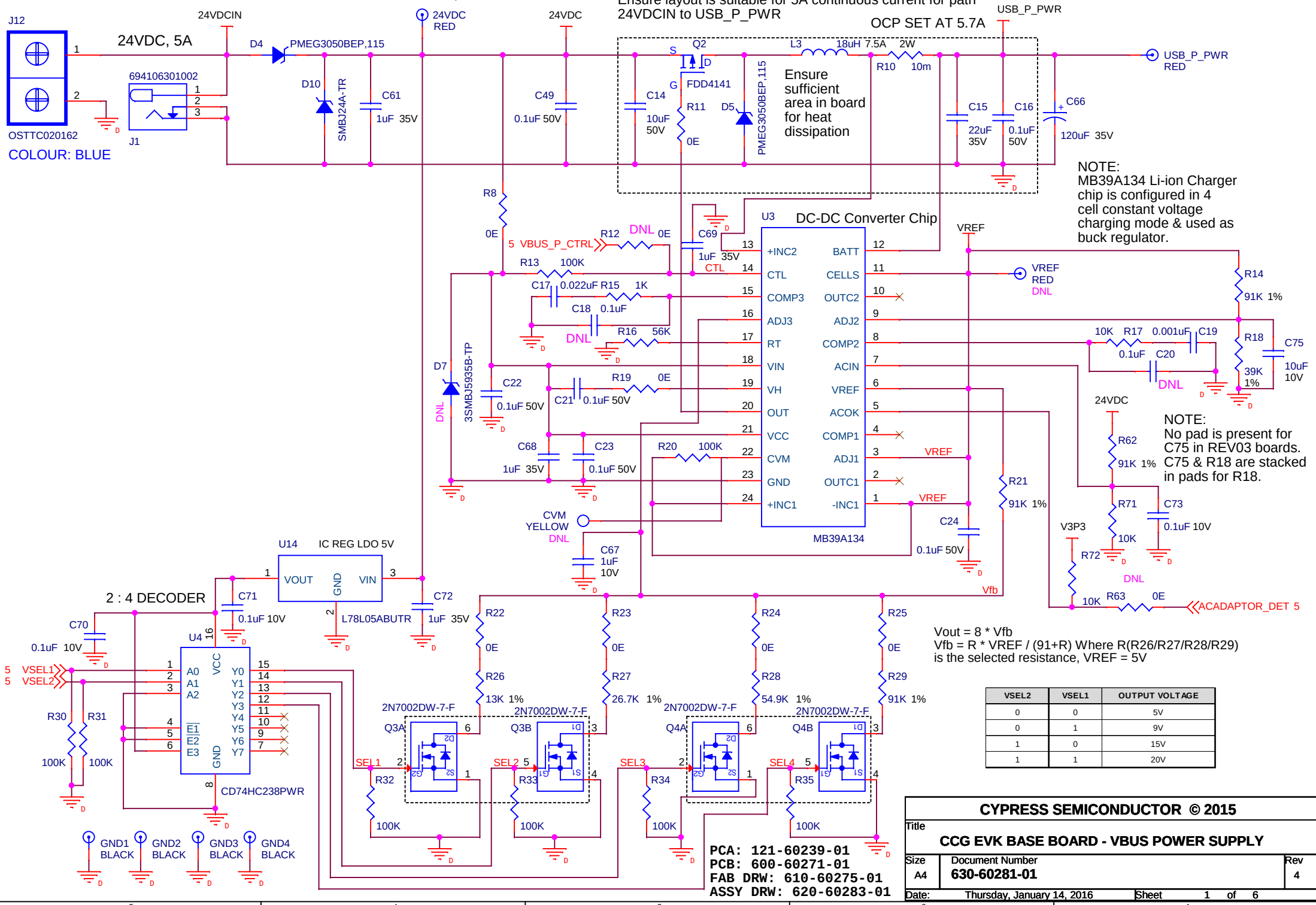
5-20V DC, 5A POWER SUPPLY

Ensure layout is suitable for 5A continuous current for path 24VDCIN to USB_P_PWR

OCP SET AT 5.7A



Vout = 8 * Vfb
Vfb = R * VREF / (91+R) Where R(R26/R27/R28/R29) is the selected resistance, VREF = 5V

VSEL2	VSEL1	OUTPUT VOLTAGE
0	0	5V
0	1	9V
1	0	15V
1	1	20V

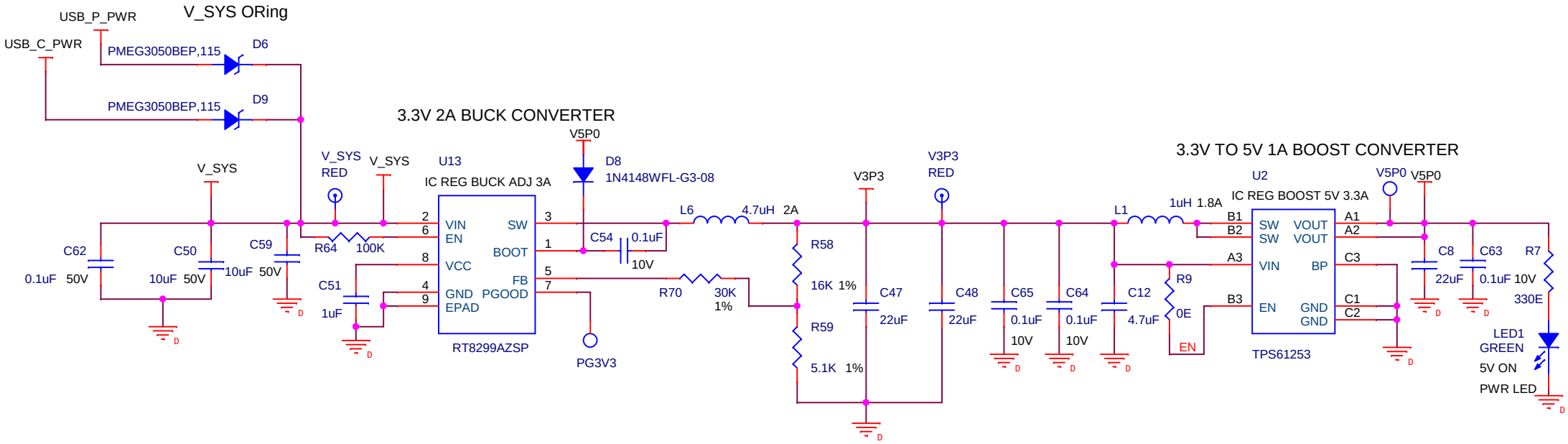
PCA: 121-60239-01
PCB: 600-60271-01
FAB DRW: 610-60275-01
ASSY DRW: 620-60283-01

CYPRESS SEMICONDUCTOR © 2015

Title: CCG EVK BASE BOARD - VBUS POWER SUPPLY

Size	Document Number	Rev
A4	630-60281-01	4

Date: Thursday, January 14, 2016 Sheet 1 of 6



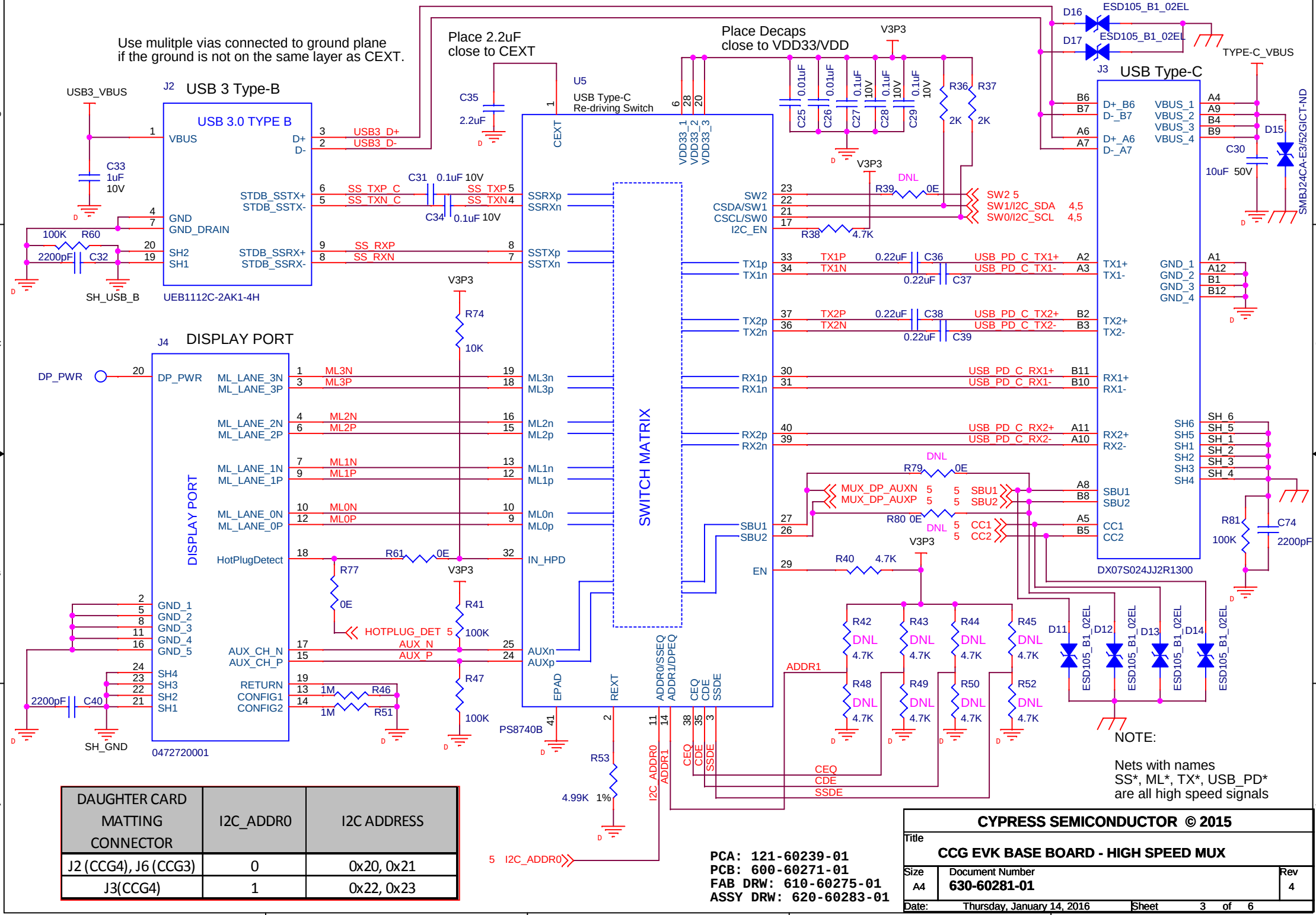
PCA: 121-60239-01
PCB: 600-60271-01
FAB DRW: 610-60275-01
ASSY DRW: 620-60283-01

CYPRESS SEMICONDUCTOR © 2015

Title: **CCG EVK BASE BOARD - 3.3V, 5V POWER**

Size	Document Number	Rev
A4	**630-60281-01**	4

Date: Thursday, January 14, 2016 Sheet 2 of 6



DAUGHTER CARD MATTING CONNECTOR	I2C_ADDR0	I2C ADDRESS
J2 (CCG4), J6 (CCG3)	0	0x20, 0x21
J3(CCG4)	1	0x22, 0x23

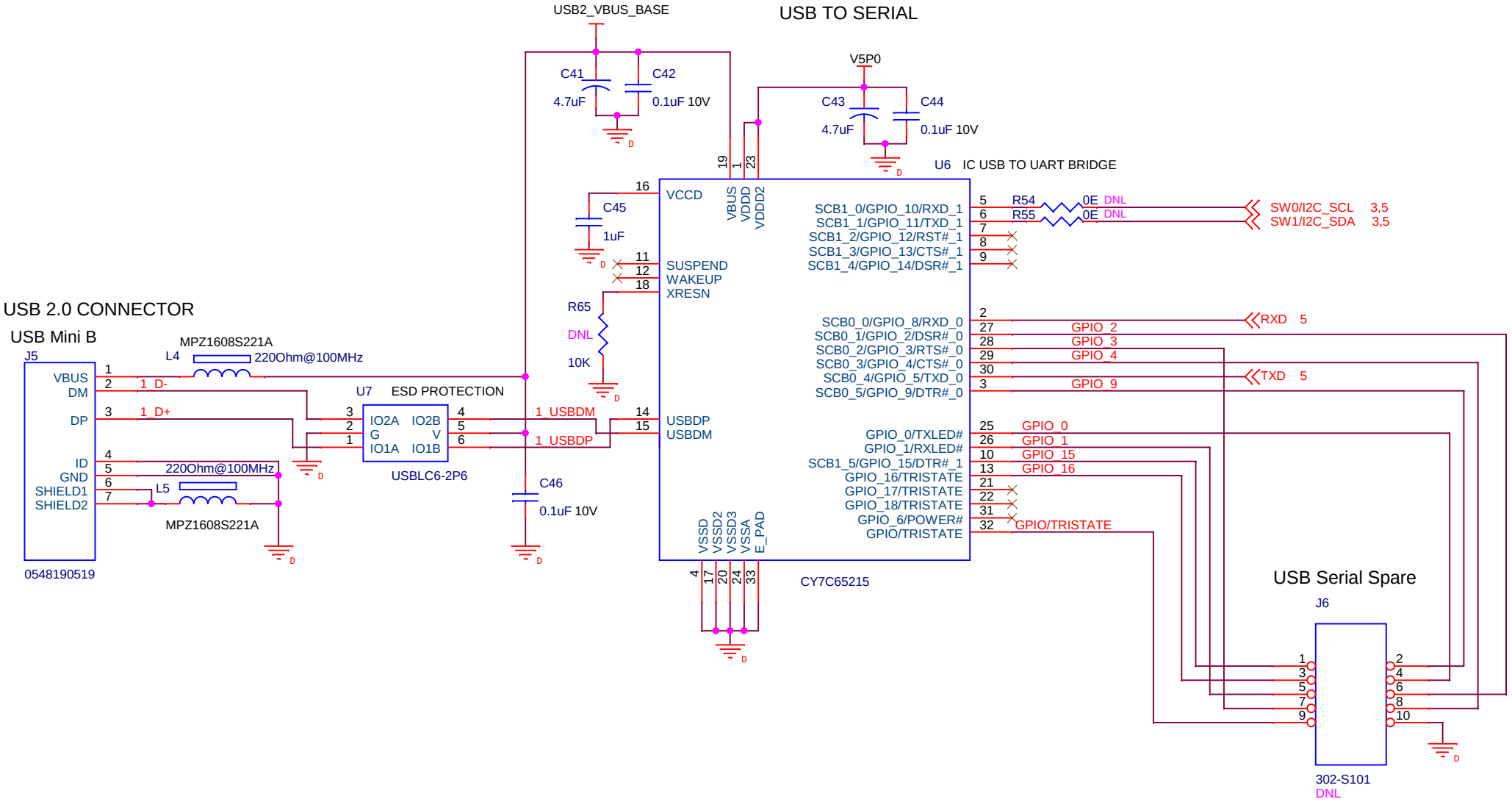
PCA: 121-60239-01
PCB: 600-60271-01
FAB DRW: 610-60275-01
ASSY DRW: 620-60283-01

CYPRESS SEMICONDUCTOR © 2015

Title: **CCG EVK BASE BOARD - HIGH SPEED MUX**

Size	Document Number	Rev
A4	**630-60281-01**	4

Date: Thursday, January 14, 2016 Sheet 3 of 6



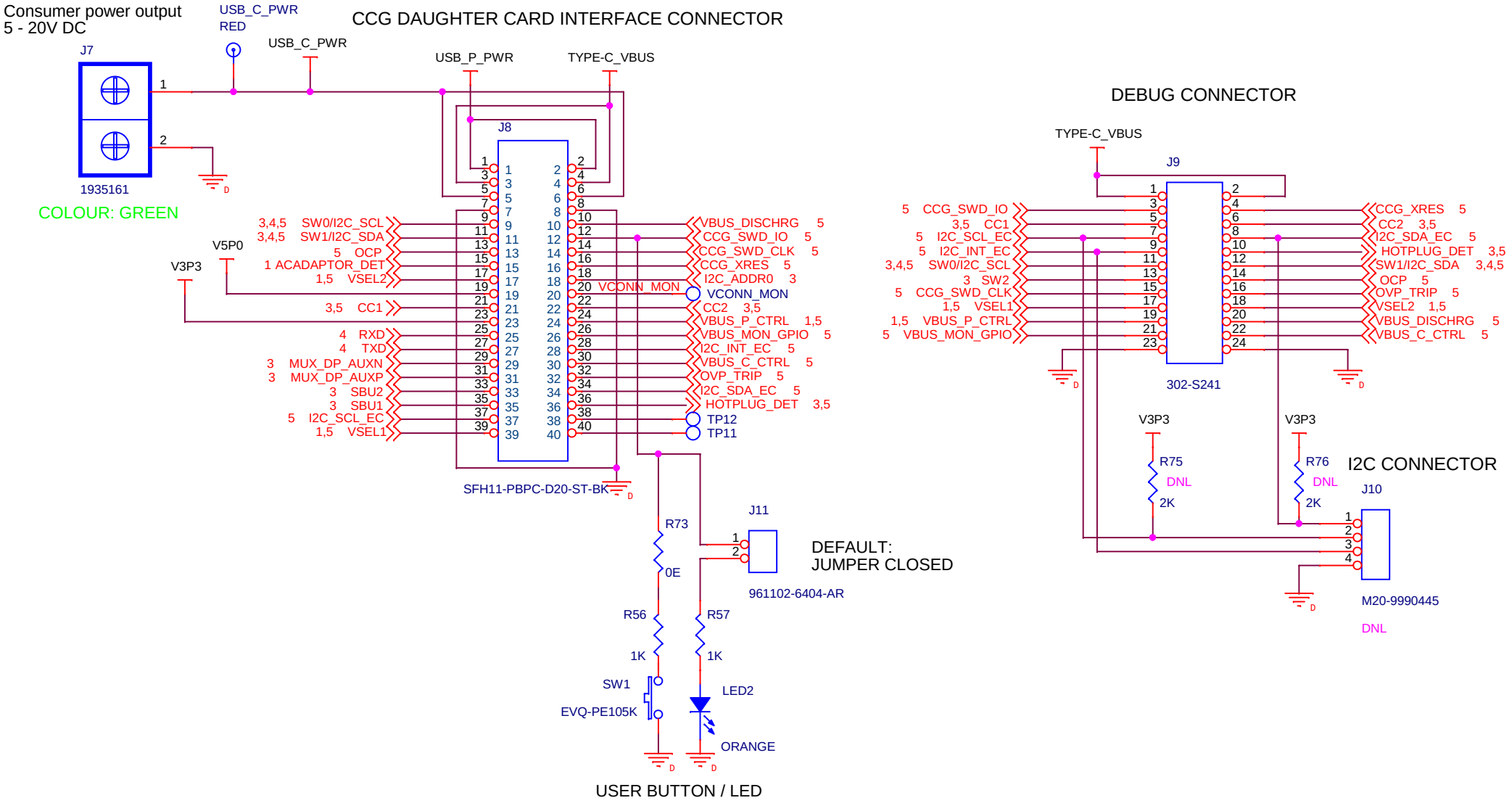
PCA: 121-60239-01
PCB: 600-60271-01
FAB DRW: 610-60275-01
ASSY DRW: 620-60283-01

CYPRESS SEMICONDUCTOR © 2015

Title: **CCG EVK BASE BOARD - USB TO SERIAL**

Size	Document Number	Rev
A4	**630-60281-01**	4

Date: Thursday, January 14, 2016 Sheet 4 of 6



PCA: 121-60239-01
PCB: 600-60271-01
FAB DRW: 610-60275-01
ASSY DRW: 620-60283-01

CYPRESS SEMICONDUCTOR © 2015

Title: **CCG EVK BASE BOARD - CCG**

Size	Document Number	Rev
A4	**630-60281-01**	4

Date: Thursday, January 14, 2016 Sheet 5 of 6

Revision	Description of the change
2.0	1. R75 AND R76 MARKED AS DNL 2. R19 CHANGED FROM 1K TO 0E 3. R17 CHANGED FROM 1K TO 10K AND C19 FROM 0.022UF TO 0.001UF 4. J1 PART NUMBER CHANGED. 5. V3P3 TEST POINT ADDED 6. FUSE (F1) REMOVED 7. CHANGED D4 PART NUMBER AS PMEG3050BEP,115 8. ORING IC U12 AND ITS CIRCUITS REMOVED AND DIODE D6, D9 ADDED INSTEAD. 9. R74 VALUE CHANGED FROM 100K TO 10K 10. R61 DNL REMOVED 11. 22V 250mA BOOST CONVERTER (U9) CIRCUITS REMOVED
3.0	1. U14 INPUT VOLTAGE (16TH PIN) CHANGED FROM 24VDCIN TO 24VDC (AFTER DIODE D4) 2. R13 VALUE CHANGED FROM 0E TO 100K 3. C68 1uF ADDED FOR U3.21 PIN 4. C69 1uF ADDED FOR U3.14 PIN 5. R8 0603 0E RESISTOR ADDED INBETWEEN 24VDC AND VCC POWER OF U3 6. D7 ZENER DIODE (3SMBJ5935B-TP) ADDED IN THE VCC POWER LINE OF U3 AND MARKED AS DNL. 7. J7 CONNECTOR PITCH CHANGED FROM 2.54MM TO 5MM 8. C61 MOVED AFTER D4 DIODE AND ZENER DIODE(D10) "SMBJ24A-TR" ADDED AFTER D4 AND C60 REMOVED. 9. C67 COMPONENT VALUE CHANED FROM 0.1uF TO 1uF 10.ESD DIODE ADDED TO SBU1, SBU2, CC1 AND CC2 NETS IN J3. 11.SH1, SH2, SH3, SH4,SH5, SH6 CONNECTED THROUGH R, C TO GROUND 12.ESD DIODE ADDED TO D+, D- NETS IN J3. 13.ESD DIODE ADDED TYPE-C_VBUS NETS IN J3.
4.0	1. C75 ADDED. 2. MINOR TEXT CHANGES DONE. 3. I2C ADDRESS SELECTION TABLE UPDATED.

PCA: 121-60239-01
PCB: 600-60271-01
FAB DRW: 610-60275-01
ASSY DRW: 620-60283-01

CYPRESS SEMICONDUCTOR © 2015

Title: CCG EVK BASE BOARD - REV HISTORY

Size	Document Number	Rev
A4	630-60281-01	4

Date: Tuesday, January 19, 2016 Sheet 6 of 6